1. (Amended) A computer-implemented method, comprising:

obtaining a description of a machine;

determining from the description whether cycles output by a resource require translation from one bus to another bus, and if so, providing a translator for the resource; and

dynamically configuring the resource for translating cycles based on the translator.

9. (Amended) The method of claim 1 wherein providing a translator for the resource includes returning cycle type information.

10. (Amended) The method of claim 1 wherein the cycle type information corresponds to I/O.

11. (Amended) The method of claim 1 wherein the cycle type information corresponds to memory.

13. (Amended) The method of claim 1 wherein dynamically configuring the resource based on the translator includes telling a driver of the resource what cycles to issue to cause an appropriate I/O cycle on the other bus.

14. (Amended) The method of claim 13 further comprising starting a driver of the resource.

15. (Amended) A system for configuring a resource to communicate with a device, comprising:

a bus bridge to which the device is connected,

a first component configured to analyze a description of the machine, and based on the description, to determine from the description whether cycles output by the resource require translation from one bus to another bus, and if so, to dynamically provide a translator for the resource based on translation that will be performed at the bus bridge; and

a second component configured to obtain the translator from the first component, and further configured to tell the resource to output translated cycles based on information in the translator.

25. (Amended) The system of claim 15 wherein the first component provides the translator to change a cycle type.

Please add claims 28-48 as follows:

28. (Added) The method of claim 1 wherein dynamically configuring the resource for translating cycles based on the translator comprises, providing a driver of the resource with data indicative of a cycle to issue on one bus when a cycle is received on another bus.

29. (Added) The method of claim 28 wherein providing a driver of the resource with data comprises providing a translation value.

30. (Added) The method of claim 29 wherein the translation value comprises a memory address offset.

31. (Added) The method of claim 28 wherein providing a driver of the resource with data comprises providing data to indicate that the cycle type will change between a memory bus cycle and an I/O bus cycle.

32. (Added) A computer-readable medium having computer-executable instructions for performing the method of claim 1.

33. (Added) A system for configuring a resource to communicate with a device, comprising:

a bus bridge to which the device is connected;

a first component configured to analyze a description of the machine, and based on the description, to provide a translator for the resource based on translation that will be performed at the bus bridge, the first component providing the translator to change a memory address; and

a second component configured to obtain the translator from the first component, and further configured to tell the resource to output cycles based on information in to the translator.

34. (Added) The system of claim 33 wherein the bus bridge comprises a CPU to PCI bridge.

35. (Added) The system of claim 33 wherein the bus bridge comprises a PCI to ISA bridge.

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36. (Added) The system of claim 33 wherein the first component comprises an ACPI driver.

37. (Added) The system of claim 33 wherein the other component comprises an operating system component.

38. (Added) The system of claim 33 wherein the description of the machine is provided in firmware information, and wherein the first component constructs a namespace from the firmware information.

39. (Added) The system of claim 33 wherein the first component performs a translation.

40. (Added) A system for configuring a resource to communicate with a device, comprising:

a bus bridge to which the device is connected;

a first component configured to analyze a description of the machine, and based on the description, to provide a translator for the resource based on translation that will be performed at the bus bridge, the first component providing the translator to change a cycle type; and

a second component configured to obtain the translator from the first component, and further configured to tell the resource to output cycles based on information in to the translator.

41. (Added) The system of claim 40 wherein the cycle type comprises I/O and is changed to memory.

42. (Added) The system of claim 40 wherein the cycle type comprises memory and is changed to I/O.

43. (Added) The system of claim 40 wherein the bus bridge comprises a CPU to PCI bridge.

44. (Added) The system of claim 40 wherein the bus bridge comprises a PCI to ISA bridge.

45. (Added) The system of claim 40 wherein the first component comprises an ACPI driver.

46. (Added) The system of claim 40 wherein the other component comprises an operating system component.

47. (Added) The system of claim 40 wherein the description of the machine is provided in firmware information, and wherein the first component constructs a namespace from the firmware information.

48. (Added) The system of claim 40 wherein the first component performs a translation.